EE2032 Electronics (2+0+2)

Class schedule: Wednesday 13:00 – 14:50 (MC165), Labs: Tuesday and Wednesday

Instructor: Dr. Murat Doğruel, mdogruel@marmara.edu.tr
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Office Hours: Tuesdays but walk-ins welcome.

Assistants: Funda Cırık Acıkaya funda.acikaya@marmara.edu.tr

Catalog Data: Semiconductor materials, diode circuit analysis. Introduction to transistor circuits, bipolar junction transistor (BJT) and field effect transistor (FET).

Pre or co-requisite: EE2031

Textbook: R. Boylestad and L. Nashelsky, Electronic Devices and Circuit Theory, 10th Ed., Prentice Hall, 2009.

Course Objectives: Develop a basic understanding of the theory and practice of electronic devices. Develop the skills necessary to analyze and design electronic circuits.

Attendance: Classroom attendance is mandatory. Pop-quizzes will be given at unscheduled and random times. Students who know that they are going to miss class should get permission in advance.

Homework: Normally due first class of the following week. No late homework. Only selected homework problems may be graded. The others will be checked for completion.

Projects: None

Honor Code: All work done on the exams will be done on your own and pledged. Project concepts and approaches may be discussed with the other students, but the work will be done by the individual project group, and considered pledged simply by its receipt. Copied solutions, even in a partial way, are not permitted, and *a negative full grade* will be assigned for such work.

Examinations: Exams will be closed book.

Grading:

Lab work	30%
Midterm	30%
Comprehensive Final	40%

Revised by: M. Doğruel, Feb. 18, 2020

EE2032 - ELECTRONICS
Spring 2020

Schedule (Tentative)

Week	**Date**	**Topic**	**Chapter**
1	Feb. 19	Circuits Review	
2	Feb. 26	Quantum Computers	
3	Mar. 4	Introduction to nonlinear circuits	
4	Mar. 11	Semiconductors	
5	Mar. 18	Semiconductor Diodes	1
6	Mar. 25	Diode Applications	2.1 – 2.6
7	Apr. 1	Diode Applications	2.7 – 2.10
		Midterm	
8	Apr. 15	Diode Applications	2.11 – 2.12
9	Apr. 22	Bipolar Junction Transistor	3
10	Apr. 29	DC Biasing BJTs	4.1 – 4.5
11	May 6	DC Biasing BJTs	4.6 – 4.11
12	May 13	BJT AC Analysis	5
13	May 20	Field Effect Transistors	6
14	May 27	FET Biasing	7
15	June 3	FET Amplifiers	8