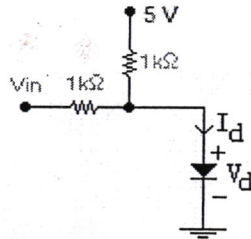


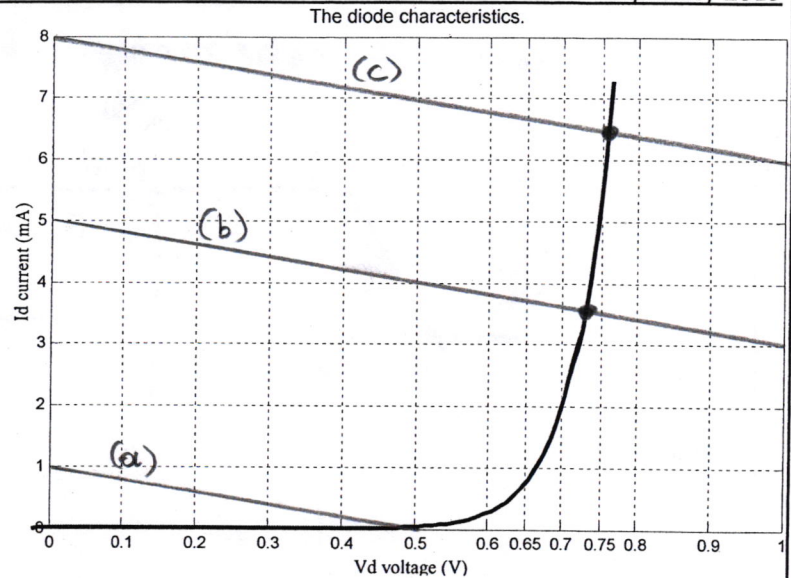
(15 pts.) 1) The diode used in the following circuit has the characteristics shown by the graph.

Draw the load line (V_d and I_d relationship from the circuit) on the graph and find V_d and I_d values (within $\pm 5\%$ range) for each of the following input voltages:

- a) $V_{in} = -4$,
b) $V_{in} = 0$,
c) $V_{in} = 3$.



Hint: To find the load line write a KCL equation involving V_{in} , V_d and I_d from the circuit.



$$\frac{V_d - V_{in}}{1K} + \frac{V_d - 5}{1K} + I_d = 0 \Rightarrow 2 \cdot V_d + 1K \cdot I_d = V_{in} + 5$$

The load line passes from the points: $V_d = 0$ & $I_d = \frac{V_{in} + 5}{1K}$, and, $I_d = 0$ & $V_d = \frac{V_{in} + 5}{2}$

These are:

	$V_d = 0$	$I_d = 0$
a)	$I_d = 1 \text{ mA}$	$V_d = 0.5 \text{ V}$
b)	$I_d = 5 \text{ mA}$	$V_d = 2.5 \text{ V}$
c)	$I_d = 8 \text{ mA}$	$V_d = 4 \text{ V}$

After we draw these lines on the figure we find that:

a) $V_d = 0.5 \text{ V}$ $I_d = 0 \text{ mA}$

b) $V_d = 0.73 \text{ V}$ $I_d = 3.5 \text{ mA}$

c) $V_d = 0.76 \text{ V}$ $I_d = 6.5 \text{ mA}$

(10 pts.) 2) For Question 1, find the required values assuming that the diode is (0.7 Volt) ideal.

The corner point $V_d = 0.7 \text{ V}$ & $I_d = 0 \text{ mA}$ is operational when

$$2 \cdot (0.7) + 1K \cdot 0 = V_{in} + 5 \Rightarrow V_{in} = -3.6 \text{ V}$$

Therefore: For $V_{in} \leq -3.6 \Rightarrow$ Diode is off
 $V_d = \frac{V_{in} + 5}{2}$, $I_d = 0$

For $V_{in} > -3.6 \Rightarrow$ Diode is on
 $V_d = 0.7$, $I_d = \frac{V_{in} + 3.6}{1K}$

We, then obtain that:

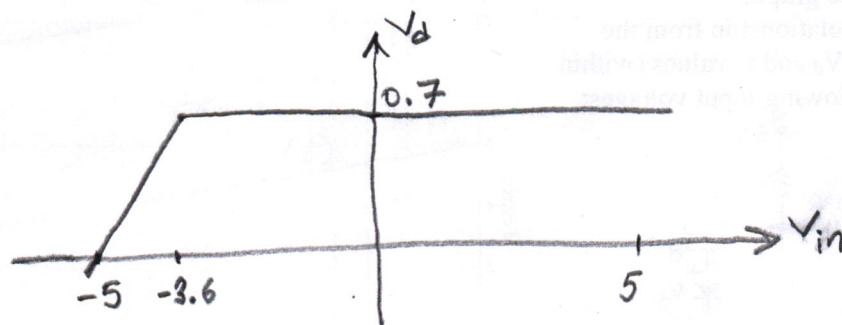
a) $V_d = 0.5 \text{ V}$ $I_d = 0 \text{ mA}$

b) $V_d = 0.7 \text{ V}$ $I_d = 3.6 \text{ mA}$

c) $V_d = 0.7 \text{ V}$ $I_d = 6.6 \text{ mA}$

(15 pts.) 3) For the circuit in Question 1, find and draw $V_d = f(V_{in})$ relationship assuming that the diode is (0.7 Volt) ideal. (When drawing the figure, use [-5 to 5] for the input voltage range.)

From the relationship obtained in Question 2 we find:



(25 pts.) 4) For the circuit given, we would like to obtain $V_1=5$ Volts and $V_2=3$ Volts. Transistors have $\beta=20$. Find the required R_1 and R_2 values.

$$I_{C2} = \frac{V_{CC} - V_1}{1K} = 5 \text{ mA}$$

$$I_{E2} = \frac{\beta+1}{\beta} \cdot I_{C2} = 5.25 \text{ mA}$$

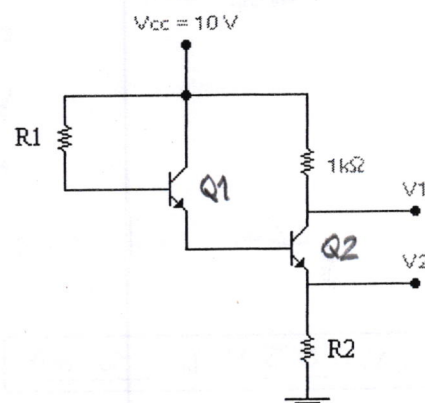
$$R_2 = \frac{V_2}{I_{E2}} = 571 \Omega$$

$$I_{E1} = I_{B2} = \frac{I_{C2}}{\beta} = 0.25 \text{ mA}$$

$$I_{B1} = \frac{I_{E1}}{\beta+1} = 11.9 \mu\text{A}$$

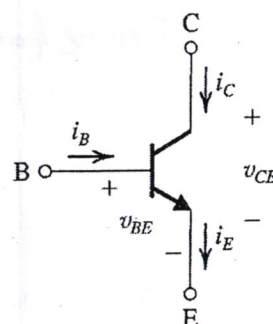
$$V_{B1} = 0.7 + 0.7 + V_2 = 4.4 \text{ V}$$

$$R_1 = \frac{V_{CC} - V_{B1}}{I_{B1}} = 470 \text{ K}\Omega$$



Hint: npn transistor equations in active mode are:

$$i_C = \beta i_B \quad V_{BE} = 0.7$$

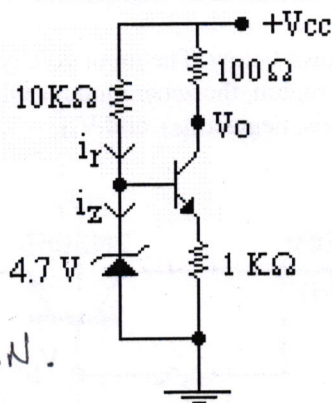


$$R_1 = 470 \text{ K}\Omega$$

$$R_2 = 571 \Omega$$

5) For the circuit in the figure, the transistor has $\beta=100$ and $V_{CE(sat)}=0.2\text{ V}$. Find the currents i_r, i_z, i_c and the voltage V_o for these cases:

(15) a) $V_{cc}=10\text{ V}$.



Hint:

A zener diode is 'on' when $I>0$, and in this case, the voltage on it is kept constant at the rating voltage, $V=V_Z$. It is 'off' when $V<V_Z$, and in this case, the current on it is zero, $I=0$.



The zener diode will be on.

$$i_r = \frac{10 - 4.7}{10\text{K}} = 0.53\text{ mA}$$

$$i_e = \frac{4.7 - 0.7}{1\text{K}} = 4\text{ mA} \Rightarrow i_c \approx 4\text{ mA}, i_b = 0.04\text{ mA}$$

$$\Rightarrow i_z = i_r - i_b = 0.49\text{ mA}$$

$$\Rightarrow V_o = V_{cc} - 100 \cdot i_c = 10 - 0.4 = 9.6\text{ V}$$

$$V_{ce} = V_o - V_e = 9.6 - (4.7 - 0.7) = 5.6\text{ V} > V_{CE(sat)} \quad \checkmark$$

$$i_r = 0.53\text{ mA}$$

$$i_z = 0.49\text{ mA}$$

$$i_c = 4\text{ mA}$$

$$V_o = 9.6\text{ V}$$

(15) b) $V_{cc}=4\text{ V}$.

The zener diode will be off. $\Rightarrow i_z=0, i_r=i_b$

$$V_{cc} = 10\text{K} \cdot i_b + 0.7 + 1\text{K} \cdot (\beta+1) \cdot i_b$$

$$4 = 10\text{K} \cdot i_b + 0.7 + 101\text{K} \cdot i_b \Rightarrow i_b = 29.7\text{ }\mu\text{A}$$

$$\Rightarrow i_c = \beta \cdot i_b = 2.97\text{ mA}$$

$$\Rightarrow V_o = V_{cc} - 100 \cdot i_c = 3.7\text{ V}$$

$$\Rightarrow V_e = 1\text{K} \cdot i_c \approx 3\text{ V}$$

$$\Rightarrow V_{ce} = V_o - V_e \approx 0.7\text{ V} > V_{CE(sat)} \quad \checkmark$$

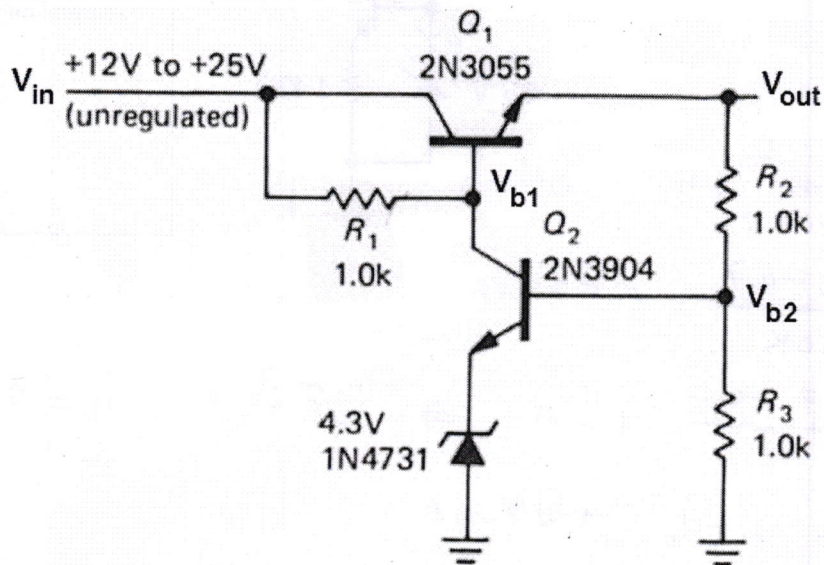
$$i_r = 29.7\text{ }\mu\text{A}$$

$$i_z = 0\text{ mA}$$

$$i_c = 2.97\text{ mA}$$

$$V_o = 3.7\text{ V}$$

(15 pts.) 6) A voltage regulator circuit is shown below. The input voltage can be between 12V to 25 V. Assume the transistors operate in the active region, the zener diode is ON, and the base currents can be neglected in your calculations. Find V_{b2} , V_{out} (when finding this, neglect I_{b2}), and V_{b1} .



$$V_{b2} = 4.3 + 0.7 = 5 \text{ V}$$

$$V_{out} = \frac{V_{b2}}{1\text{k}} \cdot (1\text{k} + 1\text{k}) = 10 \text{ V}$$

$$V_{b1} = V_{out} + 0.7 = 10.7 \text{ V}$$